


## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	34975	retention adj time'	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L2	38704	refresh	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L3	469	1 same 2	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L4	50233	dynamic adj random adj access adj memory	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L5	89255	dram	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L6	95258	4 or 5	US-PGPUB; USPAT	OR	ON	2006/05/19 11:53
L7	440	3 and 6	US-PGPUB; USPAT	OR	ON	2006/05/19 11:54
L8	22	harrand-michel.in.	US-PGPUB; USPAT	OR	ON	2006/05/19 11:54
L9	4	bulone-joseph.in.	US-PGPUB; USPAT	OR	ON	2006/05/19 11:54

Day : Friday  
Date: 5/19/2006

Time: 11:59:00


**PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = HARRAND

First Name = MICHEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">07557718</a>	<a href="#">5126734</a>	150	07/26/1990	SWITCHING MATRIX CROSSPOINT	HARRAND, MICHEL
<a href="#">07926815</a>	<a href="#">5265041</a>	150	08/07/1992	BIDIMENSIONAL FIR FILTER	HARRAND, MICHEL
<a href="#">07931827</a>	Not Issued	166	08/18/1992	IMAGE PREDICTOR	HARRAND, MICHEL
<a href="#">08111624</a>	<a href="#">5349547</a>	150	08/25/1993	BIDIMENSIONAL FIR FILTER	HARRAND, MICHEL
<a href="#">08240144</a>	<a href="#">5592428</a>	150	05/10/1994	DYNAMIC MEMORY	HARRAND, MICHEL
<a href="#">08269326</a>	<a href="#">RE35483</a>	150	06/30/1994	SWITCHING MATRIX CROSSPOINT	HARRAND, MICHEL
<a href="#">08385074</a>	<a href="#">6236683</a>	150	02/07/1995	IMAGE PREDICTOR	HARRAND, MICHEL
<a href="#">08465810</a>	<a href="#">5590070</a>	150	06/06/1995	DYNAMIC MEMORY	HARRAND, MICHEL
<a href="#">08525468</a>	<a href="#">5995513</a>	150	09/08/1995	MULTITASK PROCESSING SYSTEM	HARRAND, MICHEL
<a href="#">08990925</a>	<a href="#">6144321</a>	150	12/15/1997	MICROPROCESSOR DEDICATED TO PROCESSING OF BIT STREAMS IN A SYSTEM OF COMPRESSION/DECOMPRESSION OF ANIMATED IMAGES	HARRAND, MICHEL
<a href="#">08991022</a>	<a href="#">6133859</a>	150	12/15/1997	MICROPROCESSOR DEDICATED TO PROCESSING OF BIT STREAMS IN A SYSTEM OF COMPRESSION/DECOMPRESSION OF ANIMATED IMAGES	HARRAND, MICHEL
<a href="#">08994953</a>	<a href="#">5987488</a>	150	12/19/1997	MATRIX PROCESSOR	HARRAND, MICHEL
<a href="#">09047142</a>	Not	161	03/24/1998	CODER/DECODER OF ANIMATED	HARRAND,

	Issued			IMAGES WITH A CACHE MEMORY	MICHEL
<a href="#">09276603</a>	<a href="#">6215706</a>	150	03/25/1999	FAST STRUCTURE DRAM	HARRAND, MICHEL
<a href="#">09420129</a>	<a href="#">6914908</a>	150	10/19/1999	MULTITASK PROCESSING SYSTEM	HARRAND, MICHEL
<a href="#">09715904</a>	<a href="#">6675256</a>	150	11/17/2000	FAST DRAM CONTROL METHOD AND ADAPTED CONTROLLER	HARRAND, MICHEL
<a href="#">09730498</a>	<a href="#">6631441</a>	150	12/04/2000	DRAM READ AND WRITE CIRCUIT	HARRAND, MICHEL
<a href="#">09769009</a>	Not Issued	160	01/24/2001	Fast structure dram	HARRAND, MICHEL
<a href="#">09827298</a>	Not Issued	160	04/05/2001	Image predictor	HARRAND, MICHEL
<a href="#">09915761</a>	<a href="#">6944748</a>	150	07/26/2001	Signal processor executing variable size instructions using parallel memory banks that do not include any no-operation type codes, and corresponding method	HARRAND, MICHEL
<a href="#">09927085</a>	Not Issued	161	08/09/2001	Fast structure DRAM	HARRAND, MICHEL
<a href="#">10055510</a>	Not Issued	160	01/23/2002	Fast structure DRAM	HARRAND, MICHEL
<a href="#">10075001</a>	Not Issued	161	02/13/2002	Fast random access DRAM management method	HARRAND, MICHEL
<a href="#">10668060</a>	Not Issued	41	09/22/2003	Fast random access DRAM management method	HARRAND, MICHEL
<a href="#">10766291</a>	Not Issued	71	01/26/2004	Process for refreshing a dynamic random access memory and corresponding device	HARRAND, MICHEL
<a href="#">11018864</a>	Not Issued	30	12/21/2004	Dynamic random access memory having at least two buffer registers and method for controlling such a memory	HARRAND, MICHEL
<a href="#">11301635</a>	Not Issued	20	12/13/2005	Memory circuit, such as a DRAM, comprising an error correcting mechanism	HARRAND, MICHEL
<a href="#">11346796</a>	Not Issued	30	02/03/2006	Dynamic memory for a cellular terminal	HARRAND, MICHEL

Inventor Search Completed: No Records to Display.

Search Another: Inventor
 

Last Name	First Name
<input type="text" value="harrand"/>	<input type="text" value="michel"/>

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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = BULONE

First Name = JOSEPH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">08662778</a>	<a href="#">6138264</a>	150	06/10/1996	SYNDROME CALCULATION CIRCUIT	BULONE, JOSEPH
<a href="#">10075001</a>	Not Issued	161	02/13/2002	Fast random access DRAM management method	BULONE, JOSEPH
<a href="#">10668060</a>	Not Issued	41	09/22/2003	Fast random access DRAM management method	BULONE, JOSEPH
<a href="#">10766291</a>	Not Issued	71	01/26/2004	Process for refreshing a dynamic random access memory and corresponding device	BULONE, JOSEPH

**Inventor Search Completed: No Records to Display.**

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